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PTO/SB/21 (09-04)

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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	10/790,953
Filing Date	March 2, 2004
First Named Inventor	Larry D. Seiler
Art Unit	2857
Examiner Name	na
Attorney Docket Number	00100.02.0004

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/ Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): -IDS Cover Sheet -copies of references (5) -return postcard
<div>Remarks</div>		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Vedder, Price, Kaufman & Kammholz, P.C.		
Signature			
Printed name	Christopher J. Reckamp		
Date	October 7, 2005	Reg. No.	34,414

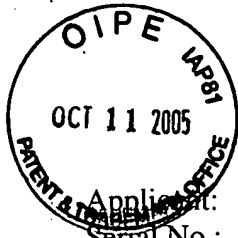
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Signature			
Typed or printed name	Christine A. Wright	Date	October 7, 2005

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Larry D. Seiler et al.

Examiner: na

Serial No.: 10/790,953

Art Unit: 2857

Filing Date: March 2, 2004

Docket No.: 00100.02.0004

Title: **APPARATUS AND METHODS FOR VOLTAGE LEVEL CONVERSION**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate of First Class Mailing
I hereby certify that this paper is being sent via first class
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10-7-05
Date

Christine A. Wright
Christine A. Wright

INFORMATION DISCLOSURE STATEMENT
IN ACCORDANCE WITH 37 CFR §§ 1.97(C) AND 1.98

Pursuant to 37 CFR §§ 1.97(c) and 1.98, Applicant respectfully submits the following statement consisting of:

1. A list of documents; and
2. General remarks.

A copy of the listed document is enclosed herewith along with Forms PTO/SB/08A and PTO/SB/08B.

Documents

- a. U.S. Patent No. 5,977,980 issued November 2, 1999 to Aleksicy;
- b. U.S. Patent No. 6,169,553 B1 issued January 2, 2001 to Fuller et al.;
- c. U.S. Patent No. 6,195,737 B1 issued February 27, 2001 to Hollister et al.;
- d. U.S. Patent No. 6,457,034 B1 issued September 24, 2002 to Morein et al.;
- e. U.S. Patent No. 6,525,726 B1 issued February 25, 2003 to Xie Feng et al.;
- f. U.S. Publication No. 2002/151606A1 published August 14, 2003 to Morein et al.;

g. International Search Report for International Application No. PCT/US2005/006744; European Patent Office; dated June 21, 2005;

h. "Per-Fragment Operations and the Framebuffer"; pages 141-148; Version 1.2.1; April 1, 1999;

i. "Hierarchical Polygon Tiling with Coverage Masks"; Ned Greene; Computer Graphics Proceedings; August 4, 1996; pages 65-74;

j. "Two-Level Hierarchical Z-Buffer for 3D Graphics Hardware"; Cheng-Hsien Chen et al.; National Chiao Tung University; 2002; pages 253-256; Taiwan, ROC; and

k. "Programming with OpenGL: Advanced Rendering, section 14: Using the Stencil Buffer"; David Blythe; from www.opengl.org; June 9, 2005.

REMARKS

No item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) before the issuance of a first office action and some of the cited references were received in connection with a foreign counterpart application. As such, there is no fee due for the filing of such information disclosure statement. If the Patent Office determines there is a fee due, the Patent Office is hereby authorized to charge any fees to Deposit Account No. 22-0259.

The submission of the above documents is not an admission that the information is prior art, analogous or otherwise material. It is respectfully requested that the above-listed documents be considered and made of record in the present application.

Date: 10-7-05

Respectfully submitted,

By: 

Christopher J. Reckamp
Registration No. 34,414

Vedder, Price, Kaufman & Kammholz, P.C.
222 N. LaSalle Street
Chicago, IL 60601
(312) 609-7500
FAX: (312) 609-5005

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Subsidiary No. 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/790,953
Filing Date	March 2, 2004
First Named Inventor	Larry D. Seiler
Art Unit	2857
Examiner Name	na
Attorney Docket Number	00100.02.0004

Sheet	1	of	2
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
		Application Number	10/790,953
		Filing Date	March 2, 2004
		First Named Inventor	Larry D. Seiler
		Art Unit	2857
		Examiner Name	na
Sheet 2	of 2	Attorney Docket Number	00100.02.0004

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		International Search Report for International Application No. PCT/US2005/006744; European Patent Office; dated June 21, 2005.	
		Per-Fragment Operations and the Framebuffer; pages 141-148; Version 1.2.1; April 1, 1999.	
		GREENE, Ned; Hierarchical Polygon Tiling with Coverage Masks; Computer Graphics Proceedings; August 4, 1996; pages 65-74.	
		CHEN, Cheng-Hsien et al.; Two-Level Hierarchical Z-Buffer for 3D Graphics Hardware; National Chiao Tung University; 2002; pages 253-256; Taiwan, ROC.	
		BLYTHE, David; Programming with OpenGL: Advanced Rendering, section 14: Using the Stencil Buffer; from www.opengl.org; June 9, 2005.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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